

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Yasushi YAMAZAKI et al.

Application No.: 09/970,766

Filed: October 5, 2001

For: METHOD OF MANUFACTURING SEMICONDUCTOR
SUBSTRATE, SEMICONDUCTOR SUBSTRATE, ELECTRO-
OPTICAL APPARATUS AND ELECTRONIC EQUIPMENT



Group Art Unit: 2812

Docket No.: 110791

INFORMATION DISCLOSURE STATEMENT

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. Relevance of the references is discussed in the present specification.
- ☒ 3. An English-language abstract of the non-English language reference is attached hereto.

Respectfully submitted,

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